

Subclass	ISSUE CLASSIFICATION
Class	
acc 09/609176	

PATENT NUM.

U.S. UTILITY Patent Application

O.I.P.E. SCANNED BK Q.A. R	PATENT DATE
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APPLICATION NO.	CONT/PRIOR	CLASS 347	SUBCLASS 355	ART UNIT 203	EXAMINER Robinson
APPLICANTS James Pavlantone 319 35 Luis Anderson Mark Hauge Steven Schieltz Kathy Chris Rudy					
TITLE Integrated circuit structure and method for its manufacture					
BEST AVAILABLE COPY					

PTO-2040
12/69

ISSUING CLASSIFICATION					
ORIGINAL		CROSS REFERENCE(S)			
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)		
INTERNATIONAL CLASSIFICATION					
<input type="checkbox"/> Continued on Issue Slip Inside File Jacket					

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	(Assistant Examiner) _____ (Date)			NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____	(Primary Examiner) _____ (Date)			ISSUE FEE	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	(Legal Instruments Examiner) _____ (Date)			Amount Due Date Paid	
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